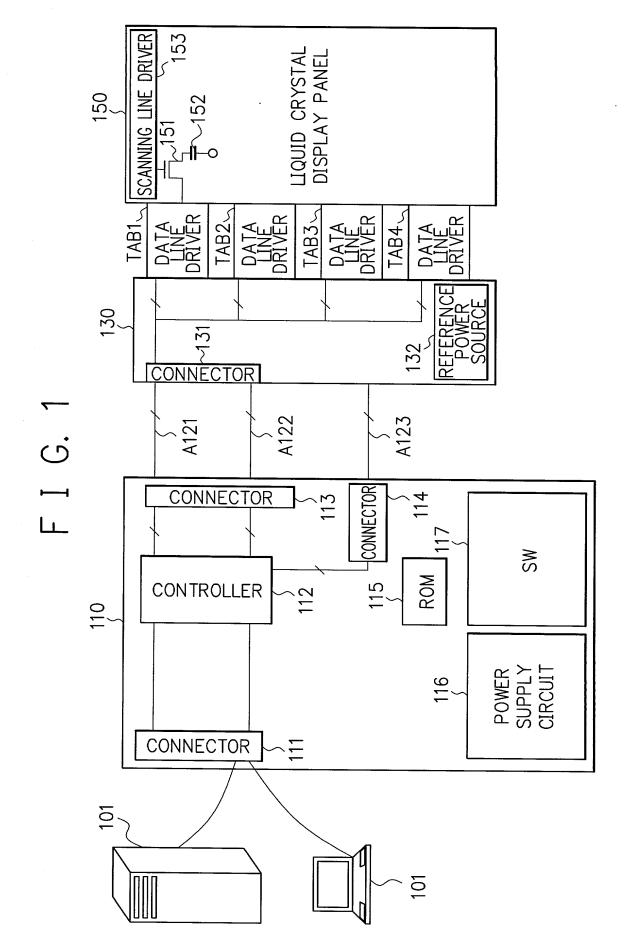
LIQUID CRYSTAL DISPLAY DEVICE . . . Enomoto et al. Greer, Burns & Crain, Ltd. (Patrick Burns) Ref. No. 1117.66092 Sheet 1 of 8 (312) 360 0080

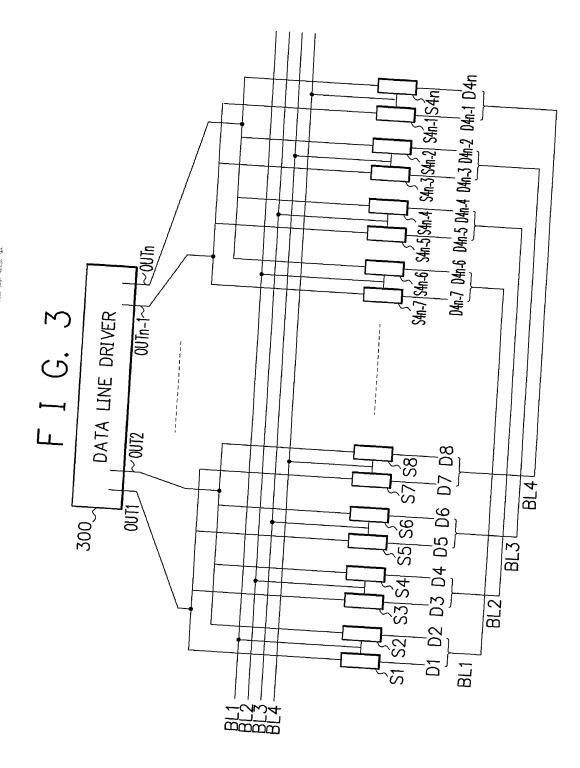


02n-1 D2n F I G. 2 PRIOR ART BĽ2 Dn+1Dn+2Dn+3Dn+4 Sn+1 Sn+2 Sn+3 Sn+4 D Sn Dn-1 S1-1 DATA LINE DRIVER BĽ D1 D2 D3 D4 00[1]00[2]00[4]

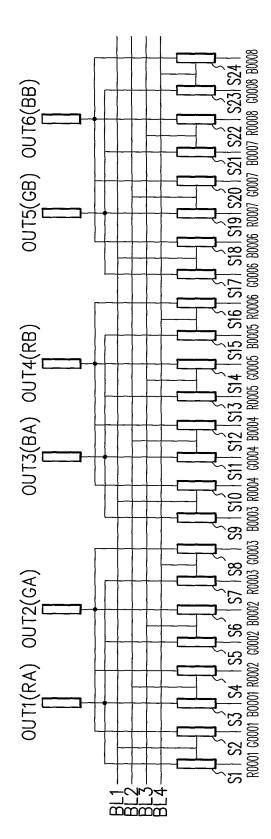
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F I G. 4



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F I G. 5

		BL1	BL2	BL3	BL4
OUT1	(RA)	R0001	B0001	G0002	R0003
OUT2	(GA)	G0001	R0002	B0002	G0003
OUT3	(BA)	B0003	G0004	R0005	B0005
OUT4	(RB)	R0004	B0004	G0005	R0006
OUT5	(GB)	G0006	R0007	B0007	G0008
OUT6	(BB)	B0006	G0007	R0008	B0008
OUT7	(RA)	R0009	B0009	G0010	R0011
OUT8	(GA)	G0009	R0010	B0010	G0011
OUT9	(BA)	B0011	G0012	R0013	B0013
OUT10	(RB)	R0012	B0012	G0013	R0014
OUT11	(GB)	G0014	R0015	B0015	G0016
OUT12	(BB)	B0014	G0015	R0016	B0016
OUT13	(RA)	R0017	B0017	G0018	R0019
OUT14	(GA)	G0017	R0018	B0018	G0019
0UT15	(BA)	B0019	G0020	R0021	B0021
<u>0UT16</u>	(RB)	R0020	B0020	G0021	R0022
OUT17	(GB)	G0022	R0023	B0023	G0024
0UT18	(BB)	B0022	G0023	R0024	B0024
					Ţ
OUT384	(BB)	B0510	G0511	R0512	B0512

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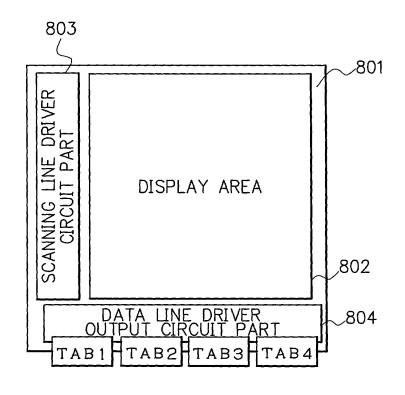
TAB3 TAB2 TAB1

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TAB1 BL1 BL2 BL3 BL4	TAB2 BL1 BL2 BL3 BL4	TAB3 BL1 BL2 BL3 BL4	TAB4 BI1 BI2 BI3 BI4
0UT1 (RA) R0001 B0001 G0002 R0003	OUT1 (RA) R0513 B0513 G0514 R0515	OUT1 (RA) R1025 B1025 G1026 R1027	81537 161538
0UT2 (GA) G0001 R0002 B0002 G0003	OUT2 (GA) G0513 R0514 B0514 G0515	0UT2 (GA) 61025 R1026 B1026 G1027	0UT2 (GA) G1537 R1538 B1538 G1539
0UT3 (BA) B0003 G0004 R0005 B0005	0UT3 (BA)	(BA)	
0UT4 (RB) R0004 B0004 G0005 R0006	0UT4 (RB)	0UT4 (RB)	OUT4 (RB)
OUT5 (GB) G0006 R0007 B0007 G0008	OUT5 (GB)	0UT5 (GB)	00T5 (GB)
OUT6 (BB) B0006 G0007 R0008 B0008	OUT6 (BB)	OUT6 (BB)	0UT6 (BB)
0UT7 (RA) R0009 B0009 G0010 R0011	OUT7 (RA)	OUT7 (RA)	OUT7 (RA)
OUT8 (GA) G0009 R0010 B0010 G0011	0UT8 (GA)	OUT8 (GA)	OUT8 (GA)
OUT9 (BA) B0011 G0012 R0013 B0013	0UT9 (BA)	0UT9 (BA)	OUT9 (BA)
OUT10 (RB) R0012 B0012 G0013 R0014	0UT10(RB)	OUT10 (RB)	OUT10(RB)
OUT11 (GB) G0014 R0015 B0015 G0016	OUT11 (GB)	0UT11 (GB)	00UT11(GB)
OUT12(BB) B0014 G0015 R0016 B0016	OUT12(BB)	0UT12(BB)	OUT12(BB)
0UT13(RA) R0017 B0017 G0018 R0019	OUT13 (RA)	0UT13(RA)	OUT13(RA)
OUT14(GA) G0017 R0018 B0018 G0019	0UT14 (GA)	0UT14(GA)	00T14(GA)
OUT15(BA) B0019 G0020 R0021 B0021	0UT15(BA)	0UT15(BA)	00T15(BA)
OUT16(RB) R0020 B0020 G0021 R0022	0UT16 (RB)	0UT16 (RB)	00T16(RB)
OUT17(GB) G0022 R0023 B0023 G0024	0U117(GB)	0UT17(GB)	00117(GB)
OUT18(BB) B0022 G0023 R0024 B0024	0UT18(BB)	OUT18(BB)	00118(88)
OUT384(BB) B0510 G0511 R0512 B0512	0UT384(BB) B1022 G1023 R1024 B1024	00T384(BB) B1534 G1535 R1536 B1536	0UT384(BB) B2046 G2047 IR2048 B2048

F I G. 8



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